

TIME TABLE

Semester: EVEN

Year: 2025-2026

Class: B. TECH-I (EC-A)

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30 – 9.20	9.30 – 10.20	10.30 – 11.20	11.30 – 12.20	12.20- 2.00	2.00 – 2.50	3.00 – 3.50	4.00 – 4.50	5.00 – 5.50
Monday			C3 (Maths-II)	D4 (NAS)					
	P1 (EEE-A1)					P11 (DLD-A1 & EC-A2)			
Tuesday			D3 (DLD)	E4 (EEE)					
	P3 (EC-A1 & EEE-A2)								
Wednesday	C1 (EC)	D2 (NAS)	E3 (IVS)	A4 (Maths-II)					
Thursday	D1 (NAS)	E2 (EEE)	A3 (Maths-II)			J5 (DLD)	K6 (EC)		
						P18 (DLD-A2)			
Friday	E1 (EEE)	A2 (NAS)	B3 (DLD)	C4 (IVS)		K5 (EC)	G6 (Maths-II)		

Sr. No.	Slot	Title	Faculty	Credit	Class Room
(1)	C1,K6,K5	Electronic Circuits (EC102)	PD,PD,PD	3-0-2	EC203N
(2)	D3,J5,B3	Digital Logic Design (EC104)	ZMP, ZMP, ZMP	3-0-2	EC203N
(3)	D4,D2,D1,A2	Network Analysis and Synthesis (EE104)		3-1-0	EC203N
(4)	E4,E2,E1	Energy and Environmental Engineering (EG110)		3-0-2	EC203N
(5)	E3,C4	Indian Value System and Social Consciousness (HS120)		2-0-0	EC203N
(6)	C3,A4,A3,G6	Mathematics-II (MA116)		3-1-0	EC203N
(7)	P3	Electronic Circuits-A1	KC,PD,L3	3-0-2	EC302N
(8)	P11	Electronic Circuits-A2	PD,PS,L3	3-0-2	EC302N
(9)	P11	Digital Logic Design-A1	ZMP,L4,P5	3-0-2	EC303N
(10)	P18	Digital Logic Design-A2	ZMP,L4	3-0-2	EC303N
(11)	P1	Energy and Environmental Engineering-A1		3-0-2	
(12)	P3	Energy and Environmental Engineering-A2		3-0-2	



TIME TABLE

Semester: EVEN

Year: 2025-2026

Class: B. TECH-I (EC-B)

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30 – 9.20	9.30 – 10.20	10.30 – 11.20	11.30 – 12.20	12.20- 2.00	2.00 – 2.50	3.00 – 3.50	4.00 – 4.50	5.00 – 5.50
Monday			C3 (Maths-II)	D4 (NAS)					
								P12 (EEE-B1)	
Tuesday			D3 (DLD)	E4 (EEE)					
						P13 (EC-B1 & DLD-B2)			
Wednesday	C1 (EC)	D2 (NAS)	E3 (IVS)	A4 (Maths-II)					
						P15 (DLD-B1 & EC-B2)		P16 (EEE-B2)	
Thursday	D1 (NAS)	E2 (EEE)	A3 (Maths-II)			J5 (DLD)	K6 (EC)		
Friday	E1 (EEE)	A2 (NAS)	B3 (DLD)	C4 (IVS)		K5 (EC)	G6 (Maths-II)		

Sr. No.	Slot	Title	Faculty	Credit	Class Room
(1)	C1,K6,K5	Electronic Circuits (EC102)	PD,PD,PD	3-0-2	EC203N
(2)	D3,J5,B3	Digital Logic Design (EC104)	ZMP, ZMP, ZMP	3-0-2	EC203N
(3)	D4,D2,D1,A2	Network Analysis and Synthesis (EE104)		3-1-0	EC203N
(4)	E4,E2,E1	Energy and Environmental Engineering (EG110)		3-0-2	EC203N
(5)	E3,C4	Indian Value System and Social Consciousness (HS120)		2-0-0	EC203N
(6)	C3,A4,A3,G6	Mathematics-II (MA116)		3-1-0	EC203N
(7)	P13	Electronic Circuits-B1	PD,L3,P16	3-0-2	EC302N
(8)	P15	Electronic Circuits-B2	KC,L3,P16	3-0-2	EC302N
(9)	P15	Digital Logic Design-B1	ZMP,L4,P5	3-0-2	EC303N
(10)	P13	Digital Logic Design-B2	ZMP,L4,P5	3-0-2	EC303N
(11)	P12	Energy and Environmental Engineering-B1		3-0-2	
(12)	P16	Energy and Environmental Engineering-B2		3-0-2	



TIME TABLE

Semester: EVEN

Year: 2025-2026

Class: B. TECH-I (VL-A)

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30 – 9.20	9.30 – 10.20	10.30 – 11.20	11.30 – 12.20	12.20- 2.00	2.00 – 2.50	3.00 – 3.50	4.00 – 4.50	5.00 – 5.50
Monday	A1 (Maths-II)	B2 (EC)	C3 (DLD)	D4 (NAS)					
						P11 (EEE-A1)			
Tuesday	B1 (EC)	C2 (DLD)	D3 (NAS)	E4 (EEE)					
Wednesday			E3 (IVS)	A4 (NAS)		I5 (DLD)	J6 (Maths-II)		
	P5 (EC-A1 & EEE-A2)								
Thursday	D1 (NAS)	E2 (EEE)	A3 (Maths-II)						
						P17 (DLD-A2)			
Friday	E1 (EEE)	A2 (Maths-II)	B3 (EC)	C4 (IVS)					
						P19 (DLD-A1 & EC-A2)			

Sr. No.	Slot	Title	Faculty	Credit	Class Room
(1)	D4,D3,A4,D1	Network Analysis and Synthesis (EE104)		3-1-0	EC202N
(2)	E4,E2,E1	Energy and Environmental Engineering (EG110)		3-0-2	EC202N
(3)	E3,C4	Indian Value System and Social Consciousness (HS120)		2-0-0	EC202N
(4)	A1,J6,A3,A2	Mathematics-II (MA116)		3-1-0	EC202N
(5)	B1,B2,B3	Electronic Circuits (VL102)	PS,PS,PS	3-0-2	EC202N
(6)	C2,C3,I5	Digital Logic Design (VL104)	RA,RA,RA	3-0-2	EC202N
(7)	P5	Electronic Circuits-A1	PS,L3,P16	3-0-2	EC302N
(8)	P19	Electronic Circuits-A2	KC,PS,L3,P16	3-0-2	EC302N
(9)	P19	Digital Logic Design-A1	JNP,RA,L4,P5	3-0-2	EC303N
(10)	P17	Digital Logic Design-A2	JNP,RA,L4	3-0-2	EC303N
(11)	P11	Energy and Environmental Engineering-A1		3-0-2	
(12)	P5	Energy and Environmental Engineering-A2		3-0-2	

